

# Exact Split Information Function for SPC

Y. Min\*, F. C. M. Lau\*<sup>†</sup> and C. K. Tse\*

\*Department of Electronic and Information Engineering, Hong Kong Polytechnic University, Hong Kong

<sup>†</sup>The Hong Kong Polytechnic University Shenzhen Research Institute

Email: encmlau@polyu.edu.hk, Fax: +852 2362 8439, Tel: +852 2766 6206

**Abstract**—Split information functions are used in deriving closed-form Extrinsic Information Transfer (EXIT) curves of super-variable nodes (SVNs) in doubly-generalized low-density parity-check (DGLDPC) codes. In this letter, we derive an exact split information function for single-parity-check (SPC) codes. The function is very easy to compute and has been verified against the results obtained using the traversal method.

**Index Terms**—Doubly generalized LDPC codes, EXIT curve, split information function.

## I. INTRODUCTION

Low-density parity-check (LDPC) codes make use of repetition codes at the variable nodes and single-parity-check (SPC) codes at the check nodes. Doubly-generalized LDPC (DGLDPC) codes are formed when the repetition codes and the SPC codes are replaced by more complex linear block codes. Subsequently, the nodes are called “super-variable nodes” (SVNs) and “super-check nodes” (SCNs). Researchers have proposed using various types of constituent codes such as Hamming codes and BCH codes in the generalized LDPC and DGLDPC codes [1]–[7].

Similar to the standard LDPC decoders, the iterative decoder of DGLDPC codes can be regarded as two concatenated component codes, including the super-variable-node (SVN) decoder and the super-check-node (SCN) decoder, as shown in Figure 1. For an  $m_a \times n_a$  adjacency matrix, the corresponding DGLDPC code has  $m_a$  SCN decoders and  $n_a$  SVN decoders which are connected by an edge-interleaver.

In each iteration, each SVN decoder takes the *channel information*  $C$  and the *a priori* information  $A_{svn}$  as the input, and then outputs the *extrinsic* information  $E_{svn}$ .  $E_{svn}$ , after passing through the edge-interleaver, becomes the *a priori* information  $A_{scn}$  of the neighboring SCN decoder. Based on  $A_{scn}$ , each SCN decoder generates the *extrinsic* information  $E_{scn}$  and passes it, via the edge-interleaver, back to the SVN decoder as the *a priori* information  $A_{svn}$ . Consequently, two types of channels, namely *communication channel* and *extrinsic channel*, exist in the decoder model.

In [8], ten Brink has proposed using Extrinsic Information Transfer (EXIT) charts to analyze the convergence behavior of turbo codes. Later, the principle of EXIT charts has been successfully applied to study other iteratively-decoded codes such as parallel concatenated codes (PCCs) [9], serially concatenated codes (SCC) [10], convolutional codes [11], LDPC codes [9], [12], repeat-accumulate codes [13], generalized LDPC codes and DGLDPC codes [7], [14], [15].

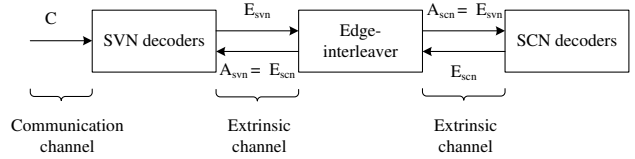


Fig. 1. An iterative decoder of DGLDPC codes.

In [16], it has been shown that codes with capacity-approaching performance over the binary erasure channel (BEC) can be designed by matching exactly the EXIT curves of the component codes. Empirically, this approach can also be applied to the more general cases, i.e., the binary-input additive-white-Gaussian-noise (BI-AWGN) channel. While the closed-form EXIT functions of LDPC codes over both the BEC and the BI-AWGN channel have been derived already, they cannot be applied to the analysis of DGLDPC directly. One common method is to use Monte Carlo simulations to estimate the EXIT charts of DGLDPC codes. However, intensive simulations have to be performed and are very time-consuming.

Another way is to derive the closed-form EXIT functions for all types of SVNs and SCNs in the DGLDPC codes over the BEC. Assume that the *communication channel* and the *extrinsic channel* are BECs and the corresponding erasure probabilities of these two channels are denoted by  $q$  and  $p$ , respectively. For a DGLDPC code, the closed-form EXIT functions of any SVN type and any SCN type over the BEC have been derived in [16]. For a general  $(n_{svn}, k_{svn})$  constituent code with code length  $n_{svn}$  and  $k_{svn}$  information bits used at the SVN, the closed-form EXIT function of this SVN over the BEC is expressed by [16]

$$I_{e,svn}^{BEC}(p, q) = 1 - \frac{1}{n_{svn}} \sum_{t=0}^{n_{svn}-1} \sum_{z=0}^{k_{svn}} p^t (1-p)^{n_{svn}-t-1} q^z (1-q)^{k_{svn}-z} \times [(n_{svn}-t)\tilde{e}_{n_{svn}-t, k_{svn}-z} - (t+1)\tilde{e}_{n_{svn}-t-1, k_{svn}-z}] \quad (1)$$

where  $\tilde{e}_{g,h}$  is named as the  $(g, h)$ -th split information function. Moreover,  $\tilde{e}_{g,h}$  is defined as the summation of the ranks of all the possible sub-matrices (denoted by  $S_{g,h}$ ) obtained by choosing  $g$  columns in the corresponding generator matrix with the size of  $k_{svn} \times n_{svn}$  and  $h$  columns in the corresponding  $k_{svn} \times k_{svn}$  identity matrix. Figure 2 depicts the definition

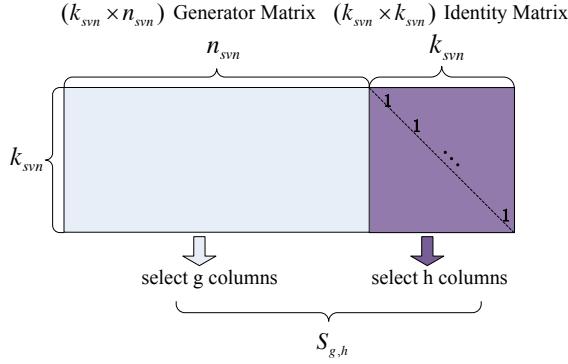


Fig. 2. The definition of the sub-matrix  $\mathbf{S}_{g,h}$  for calculating the split information function  $\tilde{e}_{g,h}$  for a general  $(n_{svn}, k_{svn})$  SVN.

of the sub-matrix  $\mathbf{S}_{g,h}$  for calculating the split information function  $\tilde{e}_{g,h}$ . For the  $(n_{svn}, 1)$  repetition code, (1) leads to

$$I_{e,svn}^{BEC}(p, q) = 1 - qp^{n_{svn}-1} \quad (2)$$

which is the well-known closed-form EXIT function for a variable node with degree- $n_{svn}$  in an LDPC code over the BEC.

Evaluating the  $(g, h)$ -th split information functions  $\tilde{e}_{g,h}$  of all constituent codes at the SVNs can be very time-consuming if closed-form formulas are not available. For a general  $(n, k)$  block code, the number of possible sub-matrices are up to  $2^{n+k}$  for computing  $\tilde{e}_{g,h}$ . Taking the  $(8, 7)$  SPC code used at the SVN as an example, in order to obtain the  $(g, h)$ -th split information function  $\tilde{e}_{g,h}$ , we should calculate the ranks of  $2^{15}$  possible sub-matrices and add them up. In this paper, we derive an exact formula for calculating  $\tilde{e}_{g,h}$  of the SPC codes at SVNs.

## II. SPLIT INFORMATION FUNCTIONS FOR SPC CODES

*Lemma 1:* Assume that

$$\begin{aligned} \mathbf{a} &= (a_1, a_2, \dots, a_i, \dots, a_k) \in \{0, 1\}^k \\ \mathbf{b} &= (b_1, b_2, \dots, b_i, \dots, b_k) \in \{0, 1\}^k \\ \mathbf{c} &= (c_1, c_2, \dots, c_i, \dots, c_k) \in \{0, 1\}^k \end{aligned}$$

and

$$c_i = \text{OR}(a_i, b_i)$$

where the  $\text{OR}(x, y)$  operator is defined as

$$\text{OR}(x, y) = \begin{cases} 0 & \text{if } x = y = 0 \\ 1 & \text{otherwise} \end{cases}$$

Then the number of possible combinations  $(\mathbf{a}, \mathbf{b})$  satisfying

$$\begin{cases} \sum c_i = t \\ \sum a_i = g \\ \sum b_i = h \end{cases} \quad (3)$$

equals

$${}_k C_t \cdot {}_t C_g \cdot {}_g C_{g+h-t}, \quad (4)$$

where  $0 \leq g, h \leq k$ ;  $\max(g, h) \leq t \leq \min(g + h, k)$ ; and  ${}_t C_h = \frac{t!}{(t-h)!h!}$ .

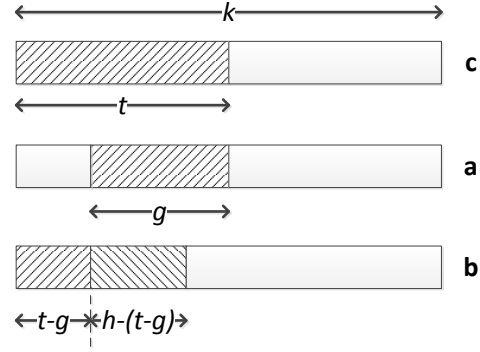


Fig. 3. The vectors  $\mathbf{c}$ ,  $\mathbf{a}$  and  $\mathbf{b}$  shown from top to bottom. The shadowed parts in the vectors indicate the positions of '1's. There are  $t$ ,  $g$  and  $h$  '1's in the vectors  $\mathbf{c}$ ,  $\mathbf{a}$  and  $\mathbf{b}$ , respectively.

*Proof:* Referring to Fig. 3, each of the vectors  $\mathbf{a}$ ,  $\mathbf{b}$  and  $\mathbf{c}$  contains  $k$  elements. Moreover,  $t$  of the elements in  $\mathbf{c}$  are '1's. First we select  $t$  out of the  $k$  locations to put the '1's, and there are  ${}_k C_t$  combinations. Among these  $t$  locations,  $g$  of them coincide with the locations where the elements of  $\mathbf{a}$  are also equal '1'. There are  ${}_t C_g$  such choices. Among the  $h$  '1's in  $\mathbf{b}$ ,  $(t - g)$  of them do not overlap (in terms of location) with those in  $\mathbf{a}$ . The remaining  $(h - (t - g)) = (g + h - t)$  '1's in  $\mathbf{b}$  overlap with the '1's in  $\mathbf{a}$ . The number of combinations is  ${}_g C_{g+h-t}$ . Therefore, the overall number of combinations satisfying (3) equals  ${}_k C_t \cdot {}_t C_g \cdot {}_g C_{g+h-t}$ .  $\square$

Denote  $\mathbf{I}_k$  as the  $k \times k$  identity matrix, and  $\mathbf{e}_k$  as the all-one vector of size  $k \times 1$ . The generator matrix  $\mathbf{G}$  of the  $(n, n - 1)$  SPC code is thus written as

$$\mathbf{G} = [\mathbf{I}_k, \mathbf{e}_k]$$

where  $k = n - 1$ . The definition of the sub-matrix  $\mathbf{S}_{g,h}$  for calculating the split information function  $\tilde{e}_{g,h}$  for the  $(n, n - 1)$  SPC code is shown in Fig. 4.

In order to calculate the split information function of the  $(n, n - 1)$  SPC code, we need to select  $g$  columns from  $\mathbf{G}$  (the LHS matrix in Fig. 4) and  $h$  columns from  $\mathbf{I}_k$  (the RHS matrix in Fig. 4) to construct a sub-matrix  $\mathbf{S}_{g,h}$ . Let  $\text{Rank}(\mathbf{W})$  denote the rank of the matrix  $\mathbf{W}$ . Then the question is how to get the sum of  $\text{Rank}(\mathbf{S}_{g,h})$  for all cases to obtain  $\tilde{e}_{g,h}$ .

*Theorem 1:* The  $\tilde{e}_{g,h}$  defined in Problem 1 is given by

$$\begin{aligned} \tilde{e}_{g,h} &= \sum_{t=\max(g,h)}^{\min(g+h,n-1)} t \cdot {}_{n-1} C_t \cdot {}_t C_g \cdot {}_g C_{g+h-t} \\ &+ \sum_{t=\max(g-1,h)}^{\min(g+h-1,n-1)} [\min(t+1, n-1) \\ &\quad \cdot {}_{n-1} C_t \cdot {}_t C_{g-1} \cdot {}_{g-1} C_{g+h-1-t}]. \end{aligned} \quad (5)$$

*Proof:* We consider two cases: (a)  $\mathbf{e}_k$  in  $\mathbf{G}$  is not selected to form  $\mathbf{S}_{g,h}$ ; (b)  $\mathbf{e}_k$  in  $\mathbf{G}$  is selected to form  $\mathbf{S}_{g,h}$ .

Case-(a):  $\mathbf{e}_k$  in  $\mathbf{G}$  is not selected. In this case, in order to construct the sub-matrix  $\mathbf{S}_{g,h}$  of size  $k \times (g + h)$ , we only need to select  $g$  and  $h$  columns from the two identity matrices

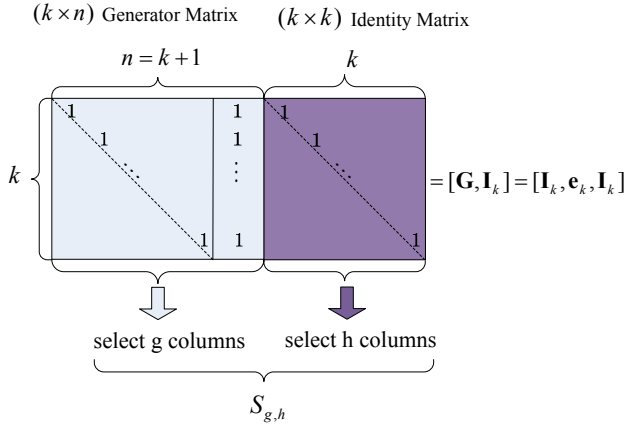


Fig. 4. The definition of the sub-matrix  $\mathbf{S}_{g,h}$  for calculating the split information function  $\tilde{e}_{g,h}$  for the  $(n, n-1)$  SPC code.

$\mathbf{I}_k$ , respectively. We denote

$$a_i = \begin{cases} 1 & \text{if the } i\text{-th column of the first } \mathbf{I}_k \text{ is selected} \\ 0 & \text{otherwise,} \end{cases}$$

$$b_i = \begin{cases} 1 & \text{if the } i\text{-th column of the second } \mathbf{I}_k \text{ is selected} \\ 0 & \text{otherwise.} \end{cases}$$

Then

$$\text{Rank}(\mathbf{S}_{g,h}) = \sum c_i = \sum \text{OR}(a_i, b_i), \quad (6)$$

where  $\sum a_i = g$  and  $\sum b_i = h$ . It is easy to see that

$$\max(g, h) \leq \text{Rank}(\mathbf{S}_{g,h}) \leq \min(g+h, k) = \min(g+h, n-1). \quad (7)$$

Based on Lemma 1, the number of combinations for selecting these columns such that  $\text{Rank}(\mathbf{S}_{g,h}) = t$  is

$${}_{n-1}C_{t-1} \cdot {}_tC_g \cdot {}_gC_{g+h-t}. \quad (8)$$

Hence the sum of the ranks for all the possible cases given in (7) is given by

$$\sum_{t=\max(g,h)}^{\min(g+h, n-1)} t \cdot {}_{n-1}C_{t-1} \cdot {}_tC_g \cdot {}_gC_{g+h-t}. \quad (9)$$

Case-(b):  $\mathbf{e}_k$  in  $\mathbf{G}$  is selected. First we select  $g-1$  columns and  $h$  columns from the two identity matrices  $\mathbf{I}_k$ , respectively, to construct  $\mathbf{S}'_{g,h}$  of size  $k \times (g-1+h)$ . From Case-(a), we know that

$$\max(g-1, h) \leq \text{Rank}(\mathbf{S}'_{g,h}) \leq \min(g-1+h, k) = \min(g-1+h, n-1) \quad (10)$$

and the number of combinations equals

$${}_{n-1}C_{\text{Rank}(\mathbf{S}'_{g,h})} \cdot \text{Rank}(\mathbf{S}'_{g,h}) C_{g-1} \cdot {}_{g-1}C_{g-1+h-\text{Rank}(\mathbf{S}'_{g,h})}.$$

Noting that when we combine  $\mathbf{S}'_{g,h}$  and  $\mathbf{e}_k$  to form  $\mathbf{S}_{g,h}$ , the rank of  $\mathbf{S}_{g,h}$  is given by

$$\text{Rank}(\mathbf{S}_{g,h}) = \min(\text{Rank}(\mathbf{S}'_{g,h}) + 1, n-1). \quad (11)$$

TABLE I  
THE SPLIT INFORMATION FUNCTION OF  $(5, 4)$  SPC CODE.

g	h	$\tilde{e}_{g,h}$	g	h	$\tilde{e}_{g,h}$
0	0	0	3	0	30
0	1	4	3	1	136
0	2	12	3	2	222
0	3	12	3	3	156
0	4	4	3	4	40
1	0	5	4	0	20
1	1	36	4	1	80
1	2	78	4	2	120
1	3	68	4	3	80
1	4	20	4	4	20
2	0	20	5	0	4
2	1	104	5	1	16
2	2	192	5	2	24
2	3	148	5	3	16
2	4	40	5	4	4

TABLE II  
THE SPLIT INFORMATION FUNCTION OF  $(6, 5)$  SPC CODE.

g	h	$\tilde{e}_{g,h}$	g	h	$\tilde{e}_{g,h}$	g	h	$\tilde{e}_{g,h}$
0	0	0	3	0	60	6	0	5
0	1	5	3	1	350	6	1	25
0	2	20	3	2	800	6	2	50
0	3	30	3	3	890	6	3	50
0	4	20	3	4	480	6	4	25
0	5	5	3	5	100	6	5	5
1	0	6	4	0	60			
1	1	55	4	1	325			
1	2	160	4	2	690			
1	3	210	4	3	720			
1	4	130	4	4	370			
1	5	30	4	5	75			
2	0	30	5	0	30			
2	1	200	5	1	150			
2	2	500	5	2	300			
2	3	600	5	3	300			
2	4	345	5	4	150			
2	5	75	5	5	30			

The summation of all  $\text{Rank}(\mathbf{S}_{g,h})$  for Case-(b) is therefore

$$\sum_{t=\max(g-1, h)}^{\min(g+h-1, n-1)} \min(t+1, n-1) \cdot {}_{n-1}C_{t-1} \cdot {}_{t-1}C_{g-1} \cdot {}_{g-1}C_{g+h-1-t}. \quad (12)$$

Combining the two cases together, we obtain the result in (5).  $\square$

In Table I and Table II, we show our numerical results  $\tilde{e}_{g,h}$  calculated by Theorem 1 for  $(5, 4)$  SPC code and  $(6, 5)$  SPC code, respectively. Our numerical results  $\tilde{e}_{g,h}$  calculated by Theorem 1 are the same as those evaluated by considering all possible combinations of the sub-matrices and their ranks.

TABLE III  
THE SPLIT INFORMATION FUNCTION OF (7, 6) SPC CODE.

g	h	$\tilde{e}_{g,h}$	g	h	$\tilde{e}_{g,h}$	g	h	$\tilde{e}_{g,h}$
0	0	0	3	0	105	6	0	42
0	1	6	3	1	750	6	1	252
0	2	30	3	2	2175	6	2	630
0	3	60	3	3	3300	6	3	840
0	4	60	3	4	2760	6	4	630
0	5	30	3	5	1200	6	5	252
0	6	6	3	6	210	6	6	42
1	0	7	4	0	140	7	0	6
1	1	78	4	1	930	7	1	36
1	2	285	4	2	2550	7	2	90
1	3	500	4	3	3680	7	3	120
1	4	465	4	4	2940	7	4	90
1	5	222	4	5	1230	7	5	36
1	6	42	4	6	210	7	6	6
2	0	42	5	0	105			
2	1	342	5	1	666			
2	2	1080	5	2	1740			
2	3	1740	5	3	2400			
2	4	1530	5	4	1845			
2	5	696	5	5	750			
2	6	126	5	6	126			

### III. CONCLUSION

We have derived an exact formula for calculating the split information function for SPC codes used at the SVN's of DGLDPC codes. The results have further been verified against those found by the traversal method (i.e., forming all possible sub-matrices, evaluating their ranks and summing them up). The exact formula can facilitate DGLDPC designers evaluating the closed-form EXIT function of SVN over the BEC and subsequently over the BI-AWGN channel.

### ACKNOWLEDGEMENTS

The work described in this paper was partially supported by a grant from the RGC of the Hong Kong SAR, China (Project No. PolyU 519011) and by the National Natural Science Foundation of China (Grant No. 61372095).

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**Yue Min** received the B.S. degree in Information Engineering from Anhui University of Science and Technology, Anhui, China in 2009. She received her M.Phil. degree at the Department of Electronic and Information Engineering in 2013 from Hong Kong Polytechnic University. She joined the Baidu (China) Co., Ltd. in 2013. Her research interest lies in the area of error-correction codes, particularly on the construction of generalized LDPC codes and the design of their fast decoders.



**Francis C. M. Lau** received the BEng (Hons) degree in electrical and electronic engineering and the PhD degree from Kings College London, University of London, UK, in 1989 and 1993, respectively. He is a Professor and Associate Head at the Department of Electronic and Information Engineering, The Hong Kong Polytechnic University, Hong Kong. He is also a Fellow of IET and a Senior Member of IEEE.

He is the co-author of *Chaos-Based Digital Communication Systems* (Heidelberg: Springer-Verlag, 2003) and *Digital Communications with Chaos: Multiple Access Techniques and Performance Evaluation* (Oxford: Elsevier, 2007). He is also a co-holder of three US patents and one pending US patent. He has published over 250 papers. His main research interests include channel coding, cooperative networks, wireless sensor networks, chaos-based digital communications, applications of complex-network theories, and wireless communications.

He served as an associate editor for *IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS II* in 2004-2005 and *IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I* in 2006-2007. He was also an associate editor of *DYNAMICS OF CONTINUOUS, DISCRETE AND IMPULSIVE SYSTEMS, SERIES B* from 2004 to 2007, a co-guest editor of *CIRCUITS, SYSTEMS AND SIGNAL PROCESSING* for the special issue *Applications of Chaos in Communications* in 2005, and an associate editor for *IEICE TRANSACTIONS* (Special Section on Recent Progress in Nonlinear Theory and Its Applications) in 2011. He has been a guest associate editor of *INTERNATIONAL JOURNAL AND BIFURCATION AND CHAOS* since 2010 and an associate editor of *IEEE CIRCUITS AND SYSTEMS MAGAZINE* since 2012. He is now serving as the past Chair of Technical Committee on Nonlinear Circuits and Systems, IEEE Circuits and Systems Society.



**Chi K. Tse** received the BEng degree with first class honors and the PhD degree from the University of Melbourne, Australia, in 1987 and 1991, respectively. He is a Fellow of IEEE and also a Fellow of the Institution of Engineers Australia.

He is presently Chair Professor of Electronic Engineering at the Hong Kong Polytechnic University, and from 2005 to 2012 he was the Head of the Department of Electronic and Information Engineering. Since 2010, he has chaired the University's Culture Promotion Committee. His research interests include

complex network applications, power electronics and nonlinear systems. He is the author of the books *Linear Circuit Analysis* (London: Addison-Wesley, 1998) and *Complex Behavior of Switching Power Converters* (Boca Raton: CRC Press, 2003), co-author of *Chaos-Based Digital Communication Systems* (Heidelberg: Springer-Verlag, 2003), *Digital Communications with Chaos* (London: Elsevier, 2006), *Reconstruction of Chaotic Signals with Applications to Chaos-Based Communications* (Singapore: World Scientific, 2007), *Sliding Mode Control of Switching Power Converters: Techniques and Implementation* (Boca Raton: CRC Press, 2012), and *Blind Source Separation and Signal Reconstruction in Sensor Networks* (Beijing: Publishing House of Electronics Industry, 2012) and co-holder of 5 US patents and 2 other pending US patents.

Prof. Tse received the L.R. East Prize from the Institution of Engineers, Australia, in 1987, the Best Paper Award from *IEEE TRANSACTIONS ON POWER ELECTRONICS* in 2001, Dynamics Days Europe Presentation Prize in 2002, and the Best Paper Award from *International Journal of Circuit Theory and Applications* in 2003. While with the Hong Kong Polytechnic University, he received the President's Award for Achievements in Research in 1997 and 2000, the Faculty Best Researcher Award in 2000, the Faculty Research Grant Achievement Award in 2004, and a few other teaching awards. He was selected an IEEE Distinguished Lecturer in 2005-06 and again in 2011-12. In 2007, he was awarded the Distinguished International Research Fellowship by the University of Calgary, Canada. In 2009, he and his co-inventors won the Gold Medal with Jurys Commendation from the International Exhibition of Inventions of Geneva, Switzerland, for a novel driving technique for LEDs, and in 2013 he was awarded another Gold Medal at the same exhibition. He has been appointed as Advisory/Guest Professor by a number of universities in China including Beijing Jiaotong University, Wuhan University, Northeastern University, Beijing Institute of Technology, etc. In 2010, he has been appointed by the Ministry of Education, China, as Chang Jiang Scholars Chair Professor (2009) at Huazhong University of Science and Technology, China. In 2011, he was appointed Honorary Professor by RMIT University, Australia, and in 2012 Honorary Professor of Northeastern University of China. In 2013, he was awarded the Gladden Fellowship by the University of Western Australia.

Currently he serves as Editor-in-Chief for the *IEEE Circuits and Systems Magazine* and Editor-in-Chief of *IEEE Circuits and Systems Society Newsletter*. He was/is an Associate Editor for the *IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS PART I: FUNDAMENTAL THEORY AND APPLICATIONS* from 1999 to 2001 and again from 2007 to 2009. He has also been an Associate Editor for the *IEEE TRANSACTIONS ON POWER ELECTRONICS* since 1999. He also serves as Editor of the *International Journal of Circuit Theory and Applications* and is on the editorial boards of the *International Journal and Bifurcation and Chaos* and of the *Journal of Systems Science*. He also served as Guest Editor for the *IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS PART I: FUNDAMENTAL THEORY AND APPLICATIONS* in 2003, Guest Associate Editor for the *IEICE Transactions on Fundamentals of Electronics, Communications and Computers* in 2004-2006, and Guest Editor for *Circuits, Systems and Signal Processing* in 2005.

From 2006 to 2012, he was on the Engineering Panel of Hong Kong Research Grants Council, and since 2009 he has been on the Joint Research Scheme Panel of Hong Kong Research Grants Council. In 2013, he was appointed to the Steering Committee of Hong Kong Quality Education Fund.