A Real-time Rectification using an Adaptive Differential Encoding for High-resolution Video

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Abstract— Previously, various rectification methods using compressed lookup table have been studied for real-time hardware stereo vision system. These loss compression methods may occur distortion that could corrupt disparity estimation process. Differentially encoded lookup table method which is lossless compression has no distortion with reasonable compression ratio. However, the method is limited to low-resolution and low warping movement. In this paper, we propose an adaptively differential encoding method. Our proposed algorithm has approximately 18–26% compression ratio which is comparable to the previous method and tolerates high-resolution video.

Keywords— real-time processing, high-resolution rectification, lookup-table, adaptive differential encoding, lossless compression

I. INTRODUCTION

A stereo vision system has four steps. The first step is rectification as a pre-processing. A role of rectification is to align epipolar lines. And the second step is disparity estimation to obtain disparity-map. In the third step, filtering techniques are utilized to enhance disparity-map. Finally, disparity-map is used for various applications such as visual fatigue decrease, object segmentation and tracking, motion control and etc. Since rectification not only aligns epipolar lines but also decreases search-range for disparity estimation process, it is essential process to achieve real-time processing. Figure 1 shows reduced search-range.

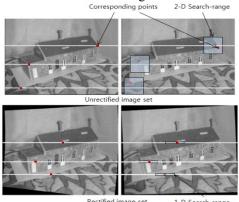


Figure 1. Example of reduced search-range[1]

Rectification could categorize into real-time calculation and pre-calculation using a LUT (Lookup table). Since real-time calculation consist a large amount of computation, many research tried to simplify calculation of epipolar geometry matrix as in [2]–[4]. And a hardware architecture using FPGA (Field-programmable gate array) is also used to achieve real-time processing as in [5]–[7]. However, simplified calculation methods are not suitable for real-time processing. And FPGA methods could process in real-time, but they consist of complex hardware architecture.

On the other hand, pre-calculation using a LUT has fast processing-time with compact hardware architecture as in [8]. But disadvantage of this method is high memory consumption. To overcome this issue, loss and lossless compression techniques are applied. One of the loss compression methods subsamples rectification mapping data and reconstructs using interpolation as in [9], [10]. Another recent research uses the run-length encoding technique with different order as in [11]. Although the loss compression method has much higher compression ratio than the lossless compression method, it also has distortion, and could be critical for the performance of disparity estimation process. To avoid distortion, reference [12] uses differential encoding and decoding using only subtraction and accumulation, respectively that consumes only 1.3Mbytes for a 1280 by 720 image with 27% compression ratio. However, it limits data magnitude bits to 3, so data magnitude have to be less than 3 which is not suitable for high-resolution rectification.

In this paper, we propose an adaptive differential encoding method. We analysed probability of appearance of differential values and built a new codebook including four modes. The first mode is comprised of minimum bit size to decrease compression ratio, and the fourth mode could extent bit size for magnitude to tolerate high-resolution video.

The rest of paper is organized as follows. We will introduce basic concept of rectification using LUT in Section II. Our proposed algorithm will be presented in Section III. Proposed hardware architecture will be presented in Section IV. Experimental result will be given in Section V, and finally, we will draw the conclusion in Section VI.

II. BASIC CONCEPT OF RECTIFICATION USING LUT

Rectification is used for various applications such as orthophoto or orthoimage as in [13], [14], multi-view camera system as in [6], [15], intelligent vehicle using stereo vision system as in [16], [17] and etc. Difference between stereo vision system and others is that position of each camera is fixed, which needs only one set of rectification parameters if there is no individual movement of each camera and uses it consistently. Therefore, we are allowed to pre-calculate rectification mapping data and rectify images by loading mapping data from LUT memory.

Rectification using LUT could divide into calibration and rectification procedures. The calibration procedure is to calculate rectification movement using intrinsic parameters, extrinsic parameters and camera calibration factors and obtain rectification mapping data. The mapping data includes coordinate information that moves pixels on the original image to the rectified image. This procedure processes in software environment. And the rectification procedure compensates input images using mapping data from the LUT memory. Since complex computation is mostly calculated in the calibration procedure, the rectification procedure could be designed compact hardware architecture and be suitable for real-time processing. However, there are four different mapping data set which are X and Y coordinate information from left and right images. And it affects high memory consumption. Therefore, each mapping data set ought to be compressed. Compressed LUT methods are simply added encoding and decoding parts before and after LUT memory, respectively. And complexity increase depends on which compression method is utilized. Compressed LUT methods classify loss and lossless compression. Figure 2 shows overall flow of rectification using LUT.

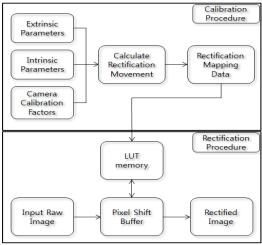


Figure 2. Overall flow of rectification using LUT

A. Loss compression

The loss compressed LUT method usually applies down-sampling to reduce a size of LUT in the encoding process and interpolation to fill void pixels in the decoding process as in [9], [10]. Given that interpolation change calculated values based on relationship with adjacent values, a rectified image is

distorted. Although rectified image using a loss compressed LUT shows reasonable PSNR over 30 dB, it could occur critical errors in the disparity estimation process. To avoid potential errors, the lossless compression is more suitable.

B. Lossless compression

The lossless compressed LUT method utilizes lossless compression technique such as Huffman, differential, runlength and etc. Among them, a differential encoding method is used with high compression ratio and compact hardware architecture as in [12]. It shows good trade-off between complexity and compression ratio. However, since it limits differential magnitude of sequential pixel values on 3 to minimize memory consumption, an error is occurred on the current coordinate if magnitude of differential value is more than 4. Moreover, when one error is occurred, it is propagated on the current line by accumulation of erroneous difference. Therefore, the previous differential encoding method operates properly only with low-resolution or low warping movement.

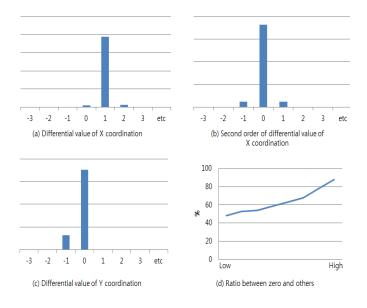


Figure 3. Distribution of difference value
(Top-left) differential X coordinate value,
(Top-right) the second order differential X coordinate value
(Bottom-left) differential Y coordinate value,
(Bottom-right) ratio between the highest probability case and others.

III. PROPOSED ALGORITHM

Adaptive compression method basically uses characteristic of input. Reference [18] adjusted sampling frequency depend on frequency of ECG (electrocardiogram) signal. Especially, ECG signal with low frequency sampled with low sampling frequency, which decrease amount of data. In the same manner, our proposed method adjusted size of memory bits to store coordinate data depend on magnitude of differential values. Size of memory bits is minimized to decrease compression ratio for the highest probability case and also maximized to have enough bit size to store maximum magnitude for the opposite case. Therefore, compression ratio is decreased with increasing the highest probability case.

As shown in figure 3(a) and 3(c), variance of differential coordinate values is distributed around 1, 0 for X and Y coordinate, respectively. And the second order differential X coordinate values are distributed around 0 in figure 3(b). A value of the highest probability case in figure 3(b) is large in comparison with the one in figure 3(a). And distribution is almost same as in figure 3(b) so that we could share the same codebook to encode X and Y coordinate values. Moreover, as shown in figure 3(d), since the ratio between the highest probability case and others increases with higher resolution, our proposed method is more suitable for higher resolution. To satisfy above-mentioned conditions, we composed a new codebook as shown in table 1.

TABLE 1. A CODEBOOK FOR ADAPTIVE DIFFERENTIAL ENCODING

Range of magnitude	0	±1 ~ ±2	±3 ~ ±6	±7~
Mode bit	00	01	10	11
Sign bit	-	1 bit	1 bit	1 bit
Magnitude bit	-	1 bit	2 bits	3 bits∼
Total bits	2 bits	4 bits	5 bits	6 bits∼

The difference value that has the highest probability case sets as mode '00' to decrease compression ratio. And to cover larger range of magnitude, the codebook could rearrange by expansion of magnitude bit in mode '11'. Let's assume that we have differential value 0 and -5. First, 0 is included in mode '00'. And mode '00' has no sign bit and magnitude bit. Therefore, 0 is encoded as '00'. Second, -5 is included in mode '10'. Sign is minus and magnitude is 5. We can assign mode bit as '10', sign bit as '1', magnitude bit as '10'. Therefore, -5 is encoded as '10110'. The encoding flow is shown in figure 4.

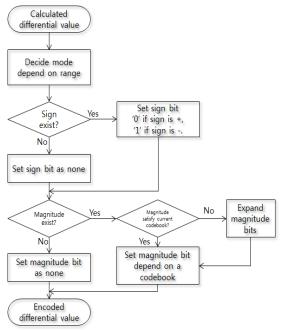


Figure 4. Encoding flow

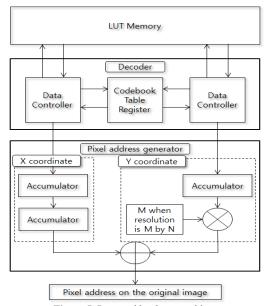
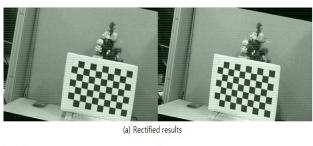


Figure 5. Proposed hardware architecture

IV. PROPOSED HARDWARE ARCHITECTURE

Our proposed hardware architecture consists of LUT memory, a decoder and a pixel address generator. First, the decoder reads differentially encoded data from LUT memory and decode into X and Y coordinate values. Then the pixel address generator reconstruct X and Y coordinate values into pixel address on the original image. The decoder consists of data controller and codebook table registers and the pixel address generator consists of two accumulators for X coordinate, one accumulators and one multiplier for Y coordinate and one adder to combine X and Y coordinate values. Overall hardware architecture is compact with low latency and simple computation, which is suitable for real-time processing. The proposed hardware architect is shown in figure 5.



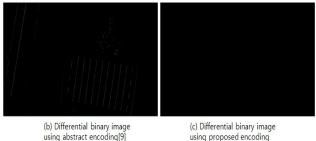


Figure 6. Differential binary image

V. EXPERIMENTAL RESULT

Our proposed method is designed using OpenCV for the encoding process and Verilog for the decoding process. For the evaluation, we applied two methods. The first one is a binary image of difference to show distortion. The equation for binarization is as:

$$B(x,y) = \begin{cases} 0 \ \textit{if Recti_img}_{(x,y)} = \textit{Ori_img}_{(x,y)}, \\ 1 \ \textit{if Recti_img}_{(x,y)} \neq \textit{Ori_img}_{(x,y)}. \end{cases}$$
 (1) Since distortion is the same as difference, distortion regions

Since distortion is the same as difference, distortion regions represent with white pixels in the result images. As shown in figure 7, the result images of the loss compression method have white regions, whereas our proposed method does not.

The second method is compression ratio. We compared to previous differential encoding method as in [12] with various resolution conditions. Compression ratio is defined as:

$$CR = \frac{Compressed\ data}{UnCompressed\ data}$$
 (2

As shown in figure 8, compression ratio of our proposed method has minimum 18.9% and maximum 26.1% lower than the previous one. And difference of compression ratio in the same resolution becomes larger with higher resolution.

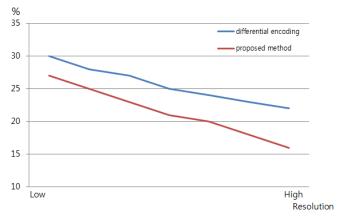


Figure 7. Compression ratio results with various resolutions

VI. CONCLUSIONS

In this paper, we proposed an adaptive differential encoding method for high-resolution video. Our proposed method effectively encodes satisfying two conditions such as minimization to decrease compression ratio and maximization to tolerate large magnitude values. And decoding process also achieves compact hardware architecture with processing in real-time. Compression ratio is approximately from 18.9% to 26.1% depending on resolution with no distortion. And the higher resolution shows the lower compression ratio which is suitable for image processing with high-resolution.

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