

Linear Leakage: Better Robustness for Spiking Neural Network

Jinming Che*, Jian Cao*, Shuo Feng*, Jue Chen*, Yuan Wang**

* School of Software & Microelectronics, Peking University, China

** School of Software & Microelectronics, Peking University, China

{chejinming, fengshuo, chenjue}@stu.pku.edu.cn, caojian@ss.pku.edu.cn, wangyuan@pku.edu.cn

(P19)Abstract—As the third-generation neural networks, Spiking Neural Networks (SNNs) have the potential to replace ANNs in noisy input scenarios due to the advantages of low power consumption and high robustness. In this paper, we propose a method that uses linear leakage on Leaky-integrate-fire (LIF) neurons of direct training of SNNs. First, this paper provides an in-depth analysis of the process of integrating and firing spikes on LIF neurons based on the linear leakage method, and finally demonstrates the inverse relationship between the leakage parameters τ_m and spiking rate ($1/T$) in linear leakage. Second, this paper designs experiments on the application of the linear leakage method to three different network models based on MNIST and Cifar10 datasets. The experimental results show that the linear leakage method in this paper has stronger robustness compared to the no-leakage and exponential leakage approaches. This finding provides a new idea with practical significance for the further study of LIF models of SNNs.

(P19)Keyword—Spiking neural networks, Spatio-temporal backpropagation, Leaky-integrate-fire neuron, Robustness, Noise attacks



Jinming Che was born in 1998, China. Currently he is a student of School of Software & Microelectronics at Peking University. His current research interests are Spiking Neural network algorithm and Neural SLAM. He joins the Intelligent Hardware and System Laboratory from 2021.



Jian Cao, Doctor of Science, is an Associate Professor at Peking University. In recent years, he has devoted himself to the research of artificial intelligence and robotics. His research areas include: artificial intelligence algorithm optimization and its hardware and software implementation, neural network model compression and hardware acceleration, computer vision, edge computing, intelligent hardware and system design. He has led and participated in many projects of Ministry of Science and Technology, National Natural Science Foundation of China, international cooperation and enterprise cooperation. He has published more than 50 SCI and EI indexed journal and conference papers and applied for more than 20 patents.



Shuo Feng was born in 1996, Beijing. Currently she is a PhD student of School of Software & Microelectronics at Peking University. Her current research interests are Artificial Intelligence and brain-inspired intelligence. She joins Key Laboratory of Microelectronic Devices and Circuits (MoE) from 2021.



Jue Chen was born in 1999, China. Currently he is a master's student of Electronic Information at School of Software and Microelectronics, Peking University. His current research interests are Spiking Neural Networks, Brain-inspired Intelligence and Intelligent System. He joins the Intelligent Hardware and System Laboratory from 2021.



Yuan Wang is a professor and doctoral supervisor at Peking University, and secretary of the Party Committee of Integrated Circuits at Peking University. He is mainly engaged in research on new paradigm intelligent computing and integrated circuit chip design. As the project leader, he has undertaken more than ten important national scientific research tasks such as the National Key Research and Development Project and the Joint Key Fund of National Natural Science Foundation of China, published more than 200 academic papers, and obtained more than 60 patent authorizations. He is currently the director of the Graduate Student Branch of China Electronics Education Society, the expert committee member of China Automotive Chip Industry Innovation Strategy Alliance, the deputy editor-in-chief of New Generation Information Technology, the deputy editor-in-chief of Integrated Circuit Industry Series, the editor of Microelectronics, and has served as the chairman and member of the program committee of international academic conferences for more than 10 times.