Design of Ka-band Chip Antenna Based on Slot Antenna

Ming-An Chung*
Department of Electronic
Engineering
National Taipei University of
Technology
Taipei, Taiwan
mingannchung@ntut.edu.tw

Kai-Xiang Chen
Department of Electronic
Engineering
National Taipei University of
Technology
Taipei, Taiwan
t112368524@ntut.edu.tw

Kuo-Chun Tseng
Department of Electronic
Engineering
National Taipei University of
Technology
Taipei, Taiwan
t110368157@ntut.org.tw

Abstract—A broadband chip antenna designed for millimeter wave (mmW) can be used in fifth-generation (5G) frequency bands. The on-chip mmW antenna designed for the Ka-band utilizes standard Complementary Metal-Oxide-Semiconductor (CMOS) technology. In this design, the mmW antenna structure is formed by connecting the top layer (Metal6) and the bottom metal layer (Metal1) to reduce losses. Many architectures have been proposed for chip antennas to overcome the metal thickness during fabrication and improve the gain of chip antennas. Therefore, this paper proposes an on-chip antenna for the fifth-generation mobile communication millimeter-wave frequency band. Using high-frequency electromagnetic simulation software, the chip antenna exhibits a minimum return loss of -18dB at 31GHz and a peak gain of -7 dB. The measured reflection coefficient is below -10dB from 18.8 GHz to 32.5 GHz.

Keyword— Antenna on-chip, CMOS, millimeter-wave



MING-AN CHUNG (Member, IEEE) received the B.Eng. and M.Eng. degrees in electronic engineering from the Chang Gung University, Taoyuan, Taiwan and the D.Eng. degree in electrical engineering from the National Taiwan University of Science and Technology (NTUST), Taipei, Taiwan, in 2003, 2005, and 2016, respectively. He is currently an Associate Professor with the Department of Electronic Engineering, National Taipei University of Technology (NTUT), where he also serves as the Leader of the Innovation Wireless Communication and Electromagnetic Applications Laboratory. His research interests include wireless communication propagation, intelligent robotics, self-driving vehicles, antenna design for various mobile and wireless communications, electromagnetic theory, and applications. He is also a Reviewer of many scientific journals, including the IEEE TRANSACTIONS ON ANTENNAS AND PROPAGATION, IEEE Transactions on Industrial Informatics, Journal of Intelligent & Robotic Systems, IET Microwaves, Antennas and Propagation, IEEE Antennas and Wireless Propagation Letters, International Review of Electrical Engineering, International Journal on Communications Antenna and Propagation and AEÜ - International Journal of Electronics and Communications, and many international conferences, including ICRA, ICCE-TW, RFIT, ICBEB, EMCAR and SNSP.



KAI-XIANG CHEN received the B.Eng. from National Taipei University of Technology (NTUT), Taipei, Taiwan, in 2023. Prior to this, he worked at Inventec as a firmware engineer, where he contributed to the development of OpenBMC, gaining substantial experience in both hardware and software domains. Currently, he is pursuing a Master's degree at National Taipei University of Technology, specializing in the field of electromagnetics. His research interests encompass antenna technology, artificial intelligence (AI), unmanned devices, and the Internet of Things (IoT).



KUO-CHUN TSENG received the B.S. degree in Computer and Communication from the National Pingtung University, Pingtung, Taiwan, in 2021. He is pursuing an M.S. degree in electronic engineering with the National Taipei University of Technology, Taiwan. His current research interest includes the design of CMOS RF/microwave integrated circuits and antenna phased arrays for the applications of the fifth generation of mobile communications.