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Speaker's Biography

Paper code Title of Paper		20120243 A Fully Integrated Pulsewidth Modulator for Class-S System		
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Speaker's Biography (Please detail below special points of background experience)

Bonghyuk Park's main research interests include UWB RF Transceiver front-end circuit design and systemlevel integration of transceivers. He posses experience in designing delta-sigma modulator (DSM) and pulsewidth modulator for mobile base-station.