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Speaker's Biography

Paper code	20120527		
Title of Paper	High-performance Implementation of a New Hash Function on FPGA		
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Speaker's Biography (Please detail below special points of background experience)			
Demin Han is a master from Hunan University of China. He received BS of Computer Science and Technology from School of Computer and Communication of Hunan University in 2009. In the same year, he got the graduate admission qualification of College of Information Science and Engineering by recommendation. His main research interests include architecture of embedded system, hardware cryptography and integrated circuit design.			
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